



PTO/SB/08a (08-03)
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 1

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of

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Complete if Known

Application Number	10/781,887
Filing Date	February 20, 2004
First Named Inventor	CONNELL, David James
Art Unit	2858 2829
Examiner Name	NWA Hollington
Attorney Docket Number	13210-23

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
J. m. H.		US-4,467,638	08-28-1984	Greenstein	
		US- 4,529,116	07-16-1985	Gutbier	
		US- 5,457,880	10-17-1995	McKinley et al.	
		US- 5,827,951	10-27-1998	Yost et al.	
		US- 5,862,973	01-26-1999	Wasserman	
		US- 2003/0041753 A1	03-06-2003	Regner et al.	
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FOREIGN PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)				
J. m. H.		JP 10256722	09-25-1998	Sony Corp		
		JP 03244188	10-30-1991	Fujitsu Ltd		
		JP 05200991	08-10-1993	Matsushita Electric Ind Co Ltd		
		DE 199 10 407 A1	09-21-2000	Siemens AG (DE)		
		EP 0 779 774 A1	06-18-1997	Nokia Mobile Phones Ltd.		
		EP 0 926 930 A2	06-30-1999	Hewlett- Packard Company		
		WO 98/42167	09-24-1998	Siemens AG (DE) et al.		

Examiner
Signature

J. m. H.

Date
Considered

9/1/04

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**INFORMATION DISCLOSURE
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Sheet 2 of 2

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Application Number	10/781,887
Filing Date	February 20, 2004
First Named Inventor	CONNELL, David James
Art Unit	2829
Examiner Name	N/A Hollington
Attorney Docket Number	13210-23

NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
g.m.h. ↓		Heraeus Benchmark II Pad Geometries and Dimensions	
		www.bomir.com/online/index.php?sub=249	
		LATHROP, RICHARD R. et al., The SMT Process Benchmarking Toolkit, Heraeus	
		LATHROP, RICHARD, "Defining Solder Paste Performance via Novel Quantitative Methods", APEX 2003,	
		Heraeus SMT Test Boards OSP and AU	

Examiner Signature	<i>John M. Hjt</i>	Date Considered	9/1/04
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